
Agenda

- 1) Walk through the progress of the projects:
 - a) DAQLink, back pressure implementation Lucien 10'
 - b) pseudo RUI and integration in XDAQ Christoph 10'
 - ?c) status of EM and RCN Ishiro ?

- 1) 2) Future of the TTC test setup
 - Claude 15'

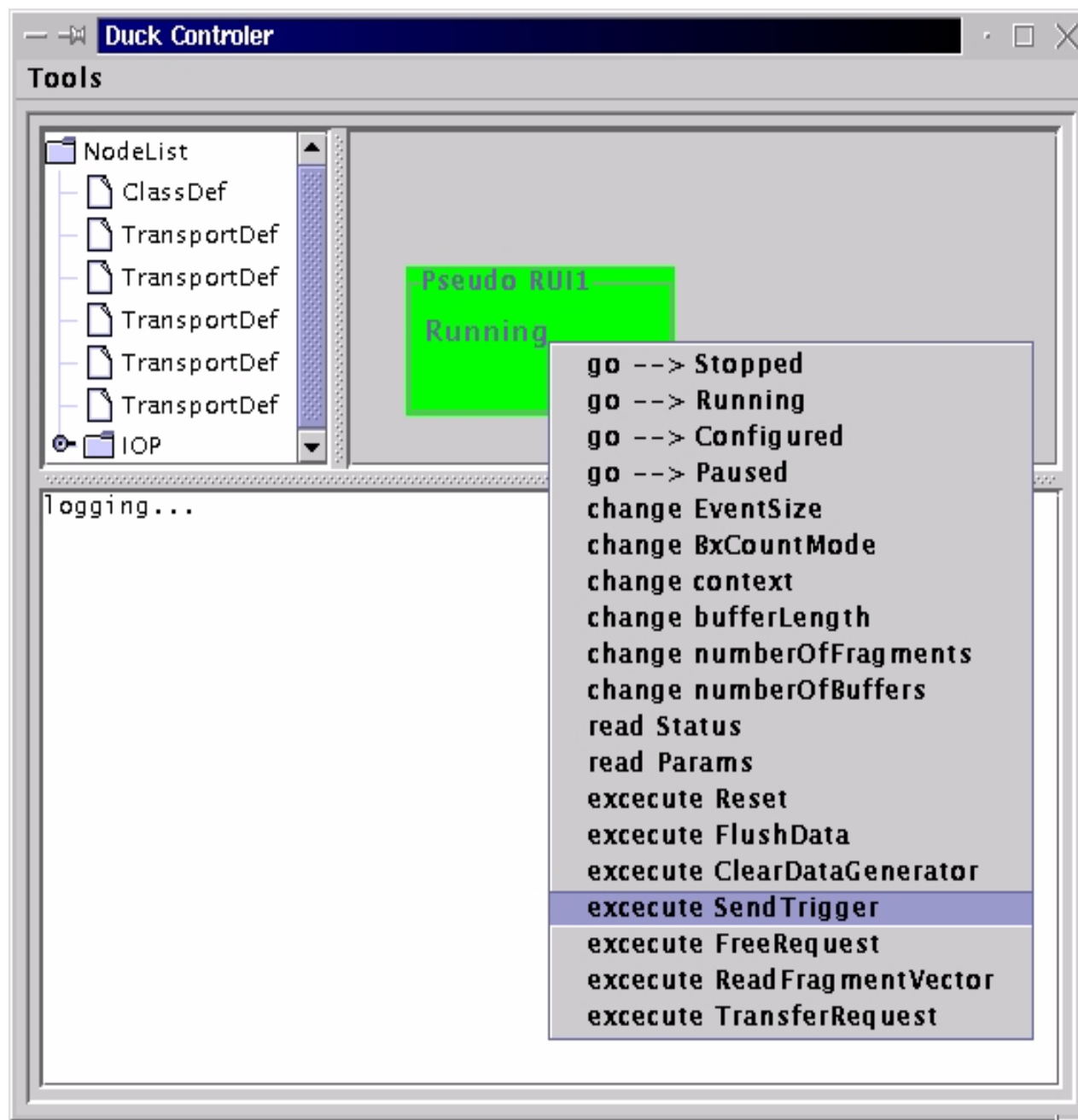
- 2) 3) Rack space in the experimental area
 - Attila 10'

Integration of XDAQ / pseudoRUI

- functionalities of the pseudoRUI
 - on PCI trigger command:
 - generate dummy data of fixed (configurable) size
 - transfer (DMA) event to internal 32MB SDRAM
 - prepare event descriptor (Startaddress, size) and write to Fifo
 - options, features:
 - data are word counts or free running counter
 - fragment size is programmable (but fixed)
 - total reset or “flush data” implemented

Implemented software

- XDAQ application level:
 - class for low level hardware access
 - XDAQ application with SOAP control access
 - all functions can be executed by SOAP commands
 - XDAQ state model and state transitions implemented
- Control software
 - based on JAVA and jxdaq classes
 - can send all possible SOAP commands to pseudoRUI
 - implements XDAQ state model
 - user interface is simple GUI
 - Gui classes are generic:
 - need to program the functionality of his component
 - need to program the actions for state changes
 - the GUI is then generated automatically,



now also implemented

- XDAQ i2o application
 - involved in data transfer
 - needs to set up DMA pseudo-RUI --> software RUM
 - DMA and interrupt of Galileo works
 - chained DMA works
 - software to interface to RUM has been developed
 - implementation has been finished
 - implementation has been tested with (light weight) test functionalities specifically implemented in the application
- To be done
 - clean up the code (classes are too big at the moment)
 - document the code and generate html documentation from it
 - test it with the software RUM

Next Steps

- One component of DAQ-column now integrated in XDAQ
- Now write the code for the other applications:
 - TTC-control
 - EM with Claude's set-up
- When link has backpressure:
 - interface pseudoRUI to link
 - develop pseudoFED (with trigger input)
- When hardware RUM is working with input and output:
 - change pseudoRUI to interface to hardware RUM