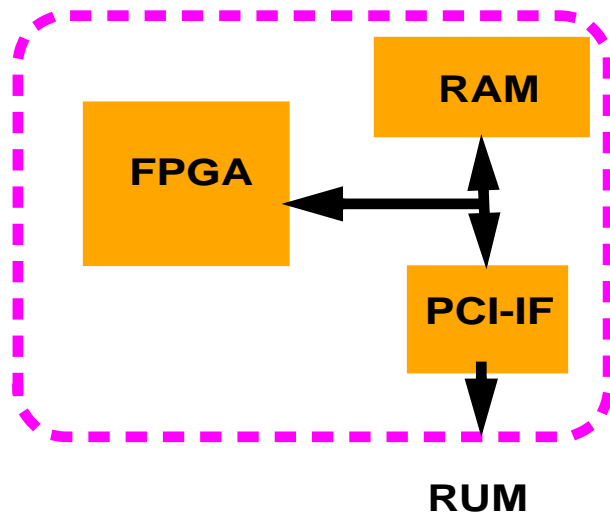


Functionality, status, etc of RUI

- Functionality



- receive trigger via DMA
- generate internally event of programmable size
- transfer event to SDRAM

- prepare a descriptor in DescriptorFifo:
 - length of event
 - startaddress of event
- software RUM Interface:
 - read # of words in DescrFifo
 - set up DMAs SDRAM-RUM
 - loop back

- Limitations

- No interface to the DAQ-Link yet
- No interface to HW/RUM implemented
- only 32 bit
- low performance (<< 100Mb/sec !)
- Cannot handle arbitrary event sizes (there must be an upper limit)
- No checks on data

- **Software**
 - OO library exists
 - some functionalities have to be added

- **ToDo**
 - implement Interface to HW RUM (interface has been defined with Georgie, Dominique)
 - implement Interface to DAQ-Link (still to be clarified)
 - write documentation
 - software integration with XDAQ (Christoph + Akos?)

- **Time schedule**
 - Interface to DAQ-Link: from the point where back-pressure exists: 2weeks full time (=> 3-4 weeks)
 - Interface to hardware RUM: 1 week full time (2-3 weeks)
 - XDAQ integration 3 weeks full time
 - integrated tests (at least RUI / software RUM stand-alone) : start august

Software

- Trigger Control
 - Trigger Control: Akos
 - Pseudo FED Control: ???
 - Rui Control : Christoph + Akos?
 - RUM Control : ???
 - software RUM: XDAQ RUM
 - BU : XDAQ BU
 - EM : Akos ?