



TriDAS-EDR



- Agenda
- Introduction part





EDR Agenda



- Overview and Introduction
 - Data-flow
 - TTS system
 - Requirements & functionality of FRL
 - data throughput
 - diagnostics capabilities: link test, event generation modes, histograms
 - local FRL DAQ
 - Requirements & functionality of FMM
 - post-mortem analysis
 - dead-time monitoring
 - worst case transition rates

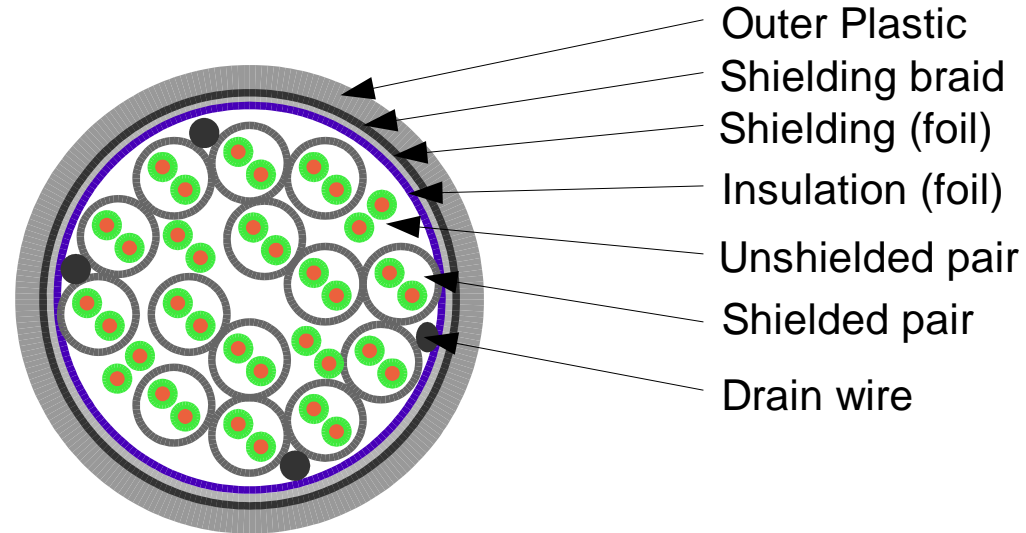


cont: EDR Agenda



- System aspects partly to be considered collaboration wide
 - These aspects are important but do not need to be completely solved before the launching of the mass production.
 - Crates and power-supplies
 - Racks (Attilas solution)
 - burn in strategy: Will be done in-place: Once the system is installed it will be kept under power. Modules are accessible: failing modules will be replaced.
 - grounding policies:
 - needs to be agreed on among all FEDs and DAQ
 - see next slides

- LVDS cable design:



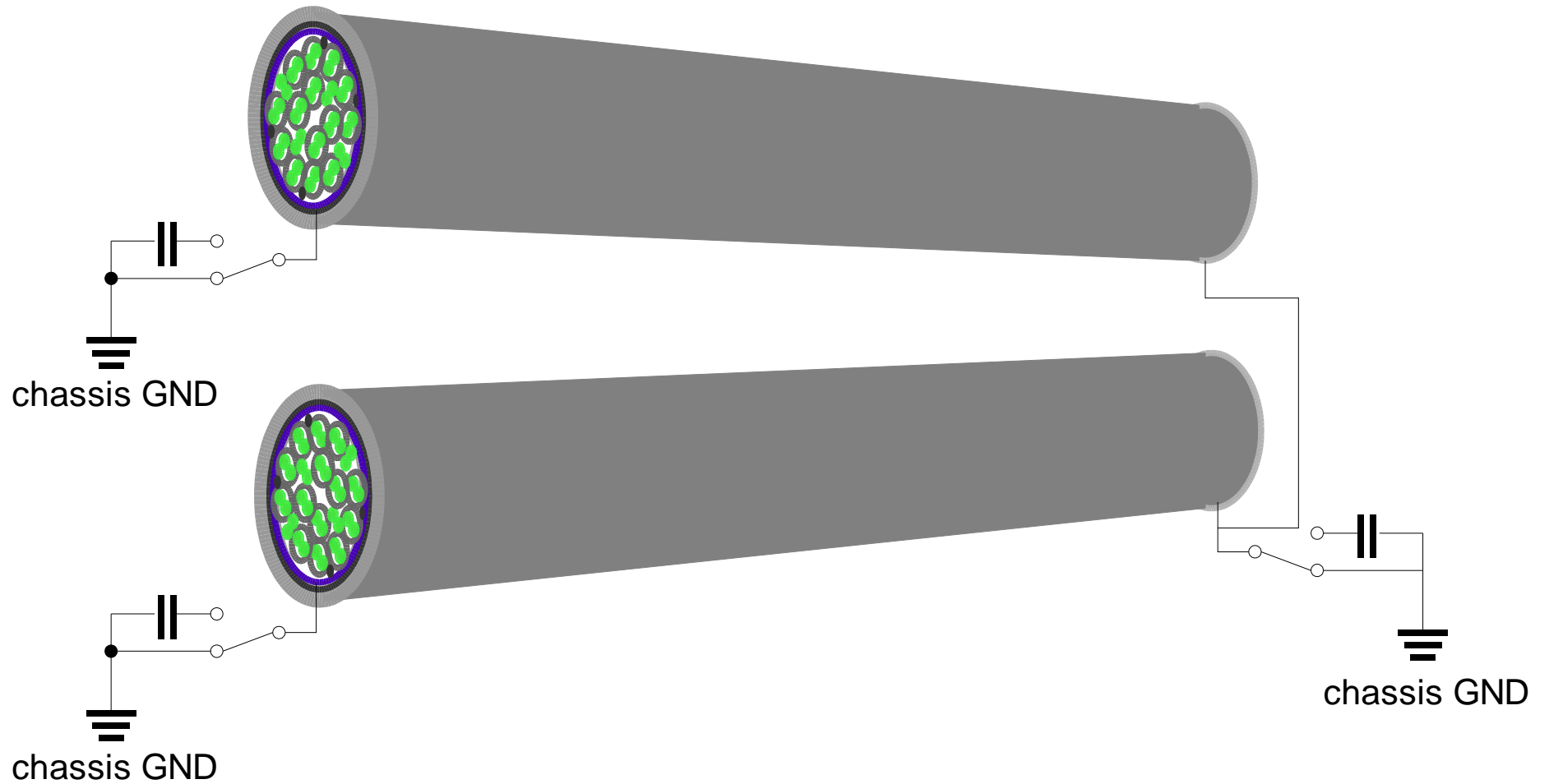
signal pairs:



outer shielding:



outer shielding:





LVDS cable: grounding



- Condition for this to work:
 - Solid ground in USC 5.
- Working example: Gigabit Ethernet
 - both ends of the outer shielding are connected directly to chassis ground
 - capacitors are avoided since they could introduce a relatively high impedance at Gbit frequencies



EDR www-page



- WWW page for EDR has been created
 - Link from TriDAS page
 - contains background information (still growing)
 - contains links to EDMS documents (hardware design)
 - Link has been given to Peter